

# Declaration of Conformity



**Equipment Type: Intel® Xeon® 3000 Sequence Processor**

**Product Codes: BX80570E3110, BX80580X3320, BX80569X3350, BX80569X3360**

The equipment described above is declared to be in conformity with the following applicable national and international standards, when tested in a representative chassis. The conformity is valid only when the equipment is used in a manner consistent with the manufacturer's recommendations and the reference documents.

Document No. / Date of Issue	Title
<b>EMC Class B:</b>	
EN 55022: 2006	Information Technology Equipment – Radio Disturbance Characteristics – Limits and Methods of Measurement
EN 55024:1998 +A1:2001 +A2:2003	Information Technology Equipment – Immunity Characteristics – Limits and Methods of Measurement
<b>Safety/Low Voltage:</b>	
EN 60950-1: 2006	Information Technology Equipment - Safety - Part 1: General Requirements

### Additional information:

#### EMC Testing Lab

Northwest EMC Inc.  
22975 NW Evergreen Parkway  
Hillsboro, OR 97124 USA

#### Safety Testing Lab

Underwriters Laboratories, Inc.  
2600 NW Lake Road  
Camas, WA 98607 USA

### Regions for which Conformity is declared:

- European Economic Area (EEA): Intel Corporation declares the equipment in compliance with the essential requirements of EC Council Directives: 2006/95/EC (LVD) and 2004/108/EC (EMC).
- Any other region where the regulatory requirements are satisfied by compliance to the standards declared above.

This Declaration of Conformity is issued by **Intel Corporation**, which is solely responsible for the declared compliance.

### Place of Issue / Declaring Company Address:

Intel Corporation  
5200 NE Elam Young Parkway  
Hillsboro, Oregon 97124 USA

Date of Issue: January 30, 2008

Tri D.Than is the manufacturer's representative with the authority of Intel Corporation management to make this Declaration.

Copies of this Declaration of Conformity may be downloaded at:  
[http://developer.intel.com/design/litcentr/ce\\_docs/index.htm](http://developer.intel.com/design/litcentr/ce_docs/index.htm)