Declaration of Conformity



This Declaration of Conformity is issued by the indicated company which is solely responsible for the declared compliance. The compliance is valid ONLY for the equipment identified when used in a manner consistent with intent of the referenced documents.

Equipment Type/Model(s): Carrier Grade Rack Mount Server, Mullins, Model Type: TSRMT2

The equipment listed is declared to be compliant to the following applicable National and International standards:

Intel Oregon Site Laboratory

5200 NE Elam Young Parkway Hillsboro, OR 97124 USA

EMC	Safety/Low Voltage:	Telecommunications	
FCC Part 15, Class A	IEC60950:1991 + Amendments 1, 2, 3, 4		
EN55022:1998 - Class A	EN60950:1992 + Amendments 1, 2, 3, 4		
EN55024:1998	UL1950, 3 rd edition		
EN61000-3-2:1995			
EN61000-3-3:1995			
CISPR22 3 rd edition:1997 - Class A			
AS/NZS 3548: 1995 - Class A			
CNS13438 TW EMI			

Test Date: October 8, 2001

Test Report No.ENFCCTSRMT2100801

Europe:			
The responsible company declares the equipment in compliance v	with Council Directive 89/336/EEC (EMC) and Council Directive 73/23/E	EEC (Safety/LVD).	
Responsible Company:	Signature of Authorized Repo	Signature of Authorized Representative:	
Intel Corporation (U.K.) Ltd, Piper's Way, Swindon, Wiltshire, SN3 1RJ England U.K.	de	Il.	
	Name: Alan McHale	Date: December 13, 2001	

Australia and N	ew Zealand:				
		nt in conformance with products required under both the Aust			requency Emissions for
Responsible Company:		Signature of Authorized Representative: (Australia)		Signature of Authorized Representative: (New Zealand)	
Intel Australia Pty. Ltd, Level 17, 111 Pacific Highway, North Sydney NSW 2060 Australia		Daville .		Gativer	
CAN: 001 798 214	ADAL 50 004 700 044	N B- :4B-II	Date: December 13,	N 0 15	
Supplier Code: N-232	ABN: 59 001 798 214	Name: David Bolt	2001	Name: Grant Finer	Date: December 13, 2001

U.S.A:		.=	
This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (this device must accept any interference received, including interference that may cause undesired operations)		rmful interference, and (2)	
Responsible Company:	Signature of Authorized Representative:		
Intel Corporation 250 Berry Hill Rd., Suite 100 Columbia, SC 29210 USA	She Clitte		
W	Name: Steve Hackett	Date: December 13, 2001	

Test Laboratory Information: