From L3NIC to QuickAssist®
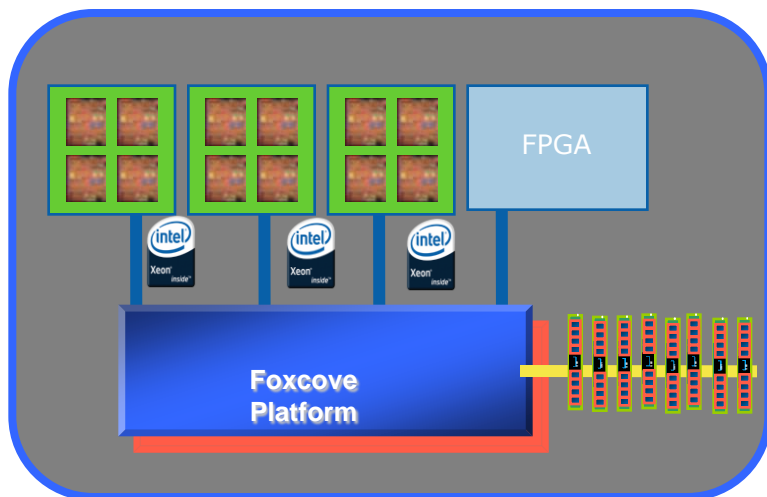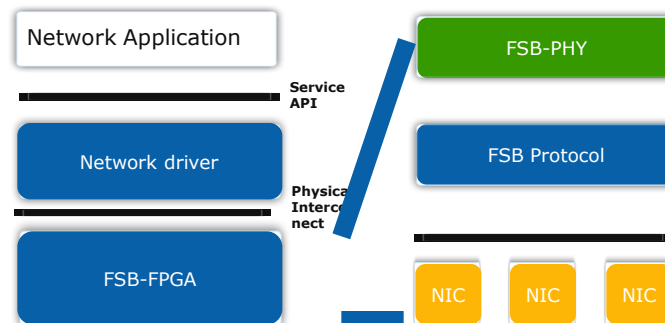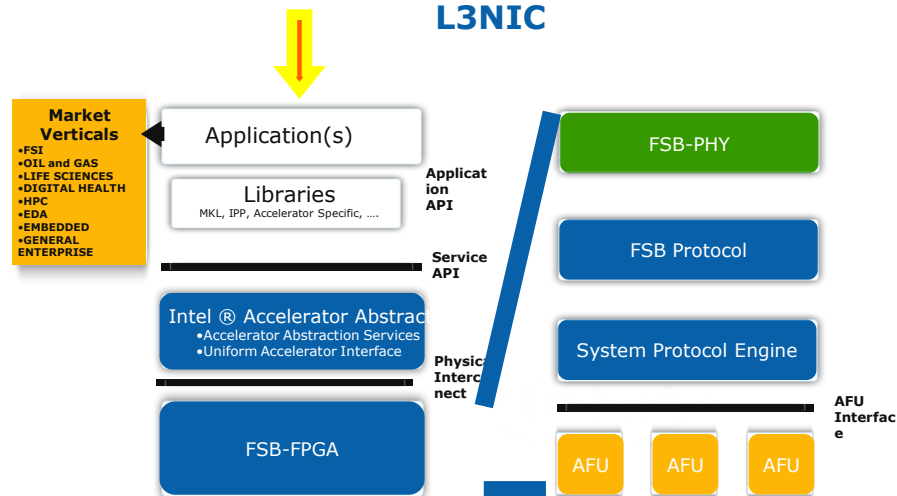

Quickassist

- **Insight from the research prototype – L3NIC**
  - High bandwidth & Low latency communication between CPU and FPGA is possible
  - Cache coherency can be done with FPGA
- **L3NIC becomes the foundation of Intel Quiassist® Technology**
  - Orders of magnitude speed up
  - Power efficient than general purpose CPU
  - Target for domain-specific applications in financial services, energy, manufacturing

L3NIC

QuickAssist Technology

Research at Intel
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