

ORBIS TERRAE COMPENDIOSA DESCRIPTIO

Quam ex Magna Vniuersali Gerardi Mercatorii Descrip̃tione Richardo Gartho, Geographiæ ac opterarum bonarum artium amatore ac fauorei summo, in veteris assidua ac familiaritate memoris Rudolphi Mercatoris fieri curauit A. M. D. LXXXVI.



**From St. Petersburg to Santa Clara
It's NANO-SCALE TO TERA-SCALE**

**Justin Rattner
Chief Technology Officer, Intel Corporation**

Research @ Intel – Mission Statement

Conduct world-class research

Deliver innovative technologies from concept to product adoption

Collaborate with industry via standards, alliances and evangelism

Engage worldwide for the best research and technology



★ St. Petersburg

Power-Efficient Reconfigurable Baseband Processor

Data Flow Programming Language and Tools

Protocol Mapping and Algorithm Refinements

Architecture, Design and Validation



Braunschweig

Barcelona

Haifa



Tera-Scale FPGA Prototyping Kit

Scalable IA-Compatible CMP Emulator

Memory Controller and Scalable Fabric

8-Core Demo Running SMP Linux

Rich set of debug features for performance analysis



清华大学
Tsinghua University



Beijing

★ Shanghai

★ Bangalore

Video Data Mining Workloads

Video content analysis

Video Mining - Face Detection/Recognition

Sports Highlights Analysis

Joint Laboratory with Tsinghua (Prof. Yang)

Carnegie Mellon

Pittsburgh



Dynamic Physical Rendering
Flexibly Conformable and Mobile Matter
Tangible Interfaces
Programmable Matter



Berkeley

Santa Clara



Ct Programming Language
Data Parallel Programming Environment
Compatible with C Language
Support Regular and Irregular Throughput App's
Scalable Across Architectures

University of Washington

Seattle

Hillsboro



Ethnography

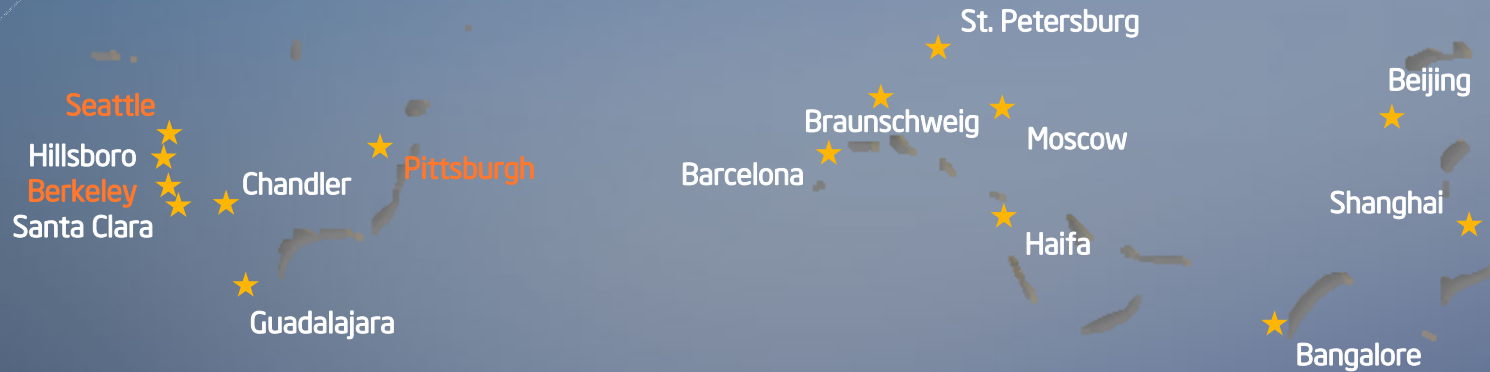
Personal Digital Money

Economic + Personal *meanings to*

Empower individuals and groups

Studies in Morocco, US, Kenya, South Africa

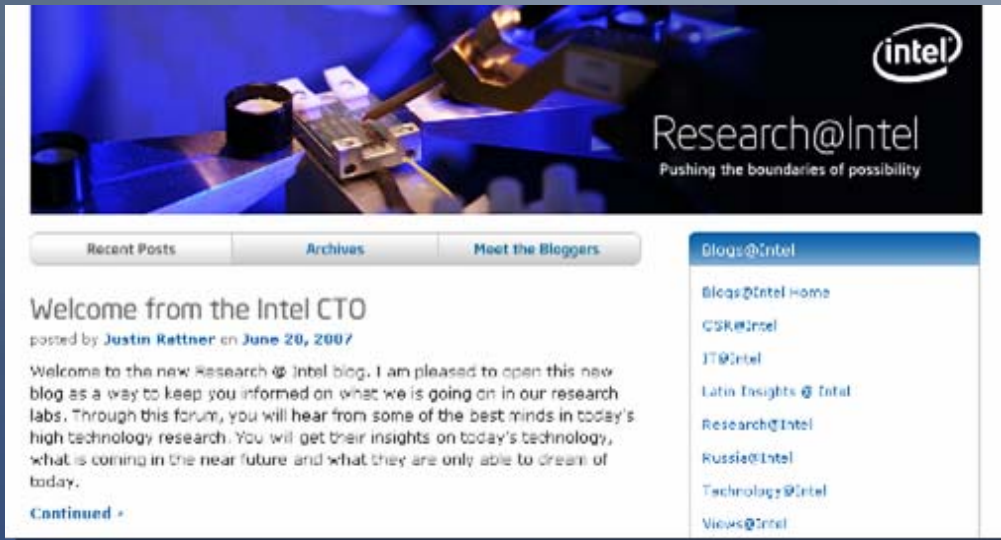
Research at Intel



- Nearly 1000 researchers
- 15 locations worldwide
- Innovative research models

Research@Intel Blog

Online as of today!



- New way to engage directly with senior Intel researchers
- Further extends Intel's blog offering (now eight)
- Initial topics will focus on tera-scale and mobility research

'Junior' is Here



Enjoy your Day!!!

Security Requests

- Visitor badge must be worn and visible at all times.
- Location of the restrooms.
- In case of emergency, please use the front lobby doors for the exit.
- All press will be required to leave the premises at 2:00pm.