EXHIBIT D

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O'MELVENY & MYERS LLP

March 5, 2009

VIA E-MAIL AND U.S. MAIL

Sogol K. Pirnazar, Esq.
Gibson Dunn & Crutcher
333 South Grand Avenue
Los Angeles, California 90071

<div align="center">

Re: *AMD v. Intel*

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Dear Sogol:

Consistent with our agreed-upon protocols regarding deposition logistics, I am providing notice of the Intel and third party depositions that AMD and the Class intend to take in April.

These requested depositions are in addition to the Intel and third party witness depositions already noticed and/or confirmed for the balance of February, March and April, including without limitation the depositions identified in my letters to you of February 9 and February 16, 2008.

First, although we do not believe CMO No. 6 governs notification of depositions the Class plaintiffs intend to take in connection with the class certification motion, out of an abundance of caution we are notifying you that Class intends to depose at least the following individuals:

Keith LeFebvre of HP
Bruce Greenwood of HP
Simon Abuyounes of PC Mall
Robert Herman or Lenovo
David P. Kaplan - Intel Expert

Second, as I indicated in a February 16, 2009 email to you, AMD reserves the right to take depositions of third parties regarding their data productions if AMD and Intel are unable to reach stipulations regarding such data productions, including whether those data productions are authentic, business records, and admissible. Class may also wish to take some such depositions in connection with the class certification motion. Accordingly, while we do not believe we are required to provide notice of these depositions at this time, out of an abundance of caution, we

are notifying you that AMD and/or Class may take depositions of the following third parties regarding their data productions:

Acer
Fujitsu
NEC
Lenovo
Sony Corp.
Sony Electronics
Dell
Toshiba
Hewlett-Packard
CompUSA
Costco
Staples
Best Buy
Office Depot
Tech Data
ASI
Ingram-Micro
CDW
Insight
PC Mall
Gateway
OfficeMax
Wal-Mart
Avnet
Synnex
Rackable
Supermicro
IBM
Apple

Third, AMD and Class intend to take a 30(b)(6) deposition of Intel regarding cost data. Specifically, AMD intends to conduct a deposition on the following topics:

I.   The existence and location of Intel databases.'
2.   Intel's policies and/or procedures for processing and recording data within Intel databases.

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[1] "Intel databases" means all databases, electronic tools, files or other internal systems containing, recording, processing, or having any relation to Intel's costs, expenses, or any other spending associated with the design, manufacturing, production, marketing or sale of Intel products.

3. The definition of data fields and data observations, abbreviations, and codes in Intel databases.
4. The existence and location of all fields maintained in Intel databases and the use and interpretation of fields in Intel databases as part of Intel's regular course of business.
5. The differences between Intel's final cost data productions and its cost data production samples, including but not limited to the rationale for including and/or excluding fields in the course of the cost data production.
6. Intel's corporate policies and practices concerning the use of Intel databases in Intel's regular course of business, including but not limited to the use of Intel databases in creating reports and preparing financial statements.
7. The existence of automated links among Intel databases, procedures for and methods of manually linking data in different Intel databases, and other possible interaclions and/or connections among Intel databases or the data in different Intel databases.
8. The separate identification of data related to Intel's microprocessor operations from data related to non-microprocessor operations, and how this identification is performed, computed, or allocated in Intel databases.
9. The calculation of Cost of Sales and product cost for various products and segments, including inventory accounting policies and application of such policies.
10. The calculation of research and development (R&D) expenses, including but not limited to methods for R&D expense allocation and the creation of R&D expense line-items (e.g., direct vs. indirect, product vs. process) as recorded in Intel databases.
11. The calculation of sales and marketing (S&M) expenses, including but not limited to methods for S&M expense allocation and the creation of marketing expense line-items (e.g., direct vs. indirect) as recorded in Intel databases.
12. The calculation of general and administrative (G&A) expenses, including but not limited to methods for G&A expense allocation and the creation of G&A expense line-items (e.g., direct vs. indirect) as recorded in Intel databases.
13. The calculation and/or allocation of depreciation & amortization expenses to various reporting segments and/or individual products, and description of how depreciation expenses are recorded in Intel databases.
14. The calculation and/or allocation of capital spending and description of how capital spending is recorded in Intel databases.
15. The calculation and/or allocation of costs at the segment, product, or any other level of disaggregation, including but not limited to the treatment of manufacturing costs, depreciation, capital spending, R&D, S&M, G&A, and any other costs or expenses recorded in Intel databases,
16. The means to link product descriptions in Intel databases (*e.g.*, ICE) to the product descriptions (*e.g.*, Pentium, Celeron) used for sales and marketing purposes.
17. The responses, including interpretation and clarification of those responses, provided by Intel to AMD's questions regarding Intel's cost data production.

Finally, AMD and Class intend to depose Kent Tibbils of ASI. We estimate the exmination will take approximately 4 hours.

Sincerely,

as for BcB

Bernard C. Barmann, Jr.
for O'MELVENY & MYERS LLP

cc: Daniel S. Floyd, Esq.
Mindy G. Davis, Esq.
Steve Fimmel, Esq.

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