



Previously Logo'd Motherboard Logo Program (PLMP)

Intel® Desktop Board

DH61DL

PLMP Report

4/20/2011

Purpose:

This report describes the DH61DL Previously Logo'd Motherboard Logo Program testing run conducted by Intel Corporation.

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Introduction

Terms and Definitions

| Term | Definitions |
|--------------------------|---|
| WHQL | Windows* Hardware Qualification Lab |
| WLK | Windows Logo Kits |
| PLMP | Previously Logo'd Motherboard Logo Program. For further information see: http://www.microsoft.com/whdc/hwtest/default.msp |
| AP Machine | Audio Precision Machine |
| Winqual | Windows Qualification |
| MSFT Tested Product List | Tested Products List. You can view the Windows Marketplace for tested products list at: http://winqual.microsoft.com/HCL/ProductList.aspx?m=v&cid=105&q=s |

Desktop Board Configuration

Desktop Board DH61DL Final Configuration Report: Completion of PLMP

Data in this section reflects system configuration at time of PLMP submission.

Board Information

| | | |
|---|--|------------------------------------|
| Product Code ¹ | BIOS String/Model | Technologies NOT Logo'd (yet) |
| DH61DL | BEH6110H.86A.0020.2011.0218.1538 | N/A - all technologies logo'd |
| Processor | | |
| Speed | 3.30GHz | |
| Family | Intel Core i5 CPU 2500K | |
| Bus Speed | 100 MHz | |
| Motherboard | | |
| Board AA # | G14066 | |
| Board FAB # | 202 (eg. 10x for fab A, 20x for fab B and etc) | |
| <i>* This report applies to the production FAB revision; Please consult your Intel Corporation representative to clarify the motherboard revision you intend to perform logo testing if not the same.</i> | | |
| System Memory | | |
| Speed | Dual Channel, DDR3, 1333MHz | |
| Memory Type | DIMM | |
| Connector Type | DDR3, 240 Pin | |
| Power Management | | |
| BIOS Default | S3 | |
| Operating System Tested | | |
| | Check Tested | Comments |
| Windows 7 and 64-bit | <input checked="" type="checkbox"/> | Windows 7 Ultimate |
| Windows Vista and 64-bit | <input type="checkbox"/> | Vista Ultimate with Service Pack 2 |
| Windows Vista Basic and 64-bit | <input type="checkbox"/> | Vista Basic with Service Pack 2 |

¹ These are the product names to enter in the "Submission ID of previously logo'd qualified PC system or server" field during your "System Using a Previously Logo'd Motherboard" submission to Microsoft.

Onboard Integrated Devices and Driver for Windows 7 32-bit and 64-bit

| Technology | OS | Version | Package version |
|--|------------------|---------------|-------------------------------------|
| Chipset Update Utility Intel® Chipset Software Utility | Windows 7 | V9.2.0.1019 | INF_allIOS_9.2.0.1019_PV |
| | Windows 7 64-bit | V9.2.0.1019 | INF_allIOS_9.2.0.1019_PV |
| Graphics Intel(R) HD Graphics Family | Windows 7 | V8.15.10.2291 | GFX_Vista32_Win7_32_8.15.10.2291_PV |
| | Windows 7 64-bit | V8.15.10.2291 | GFX_Vista64_Win7_64_8.15.10.2291_PV |
| Audio Realtek High Definition Audio | Windows 7 | V6.0.1.6299 | AUD_Vista_Win7_6.0.1.6299_PV |
| | Windows 7 64-bit | V6.0.1.6299 | AUD_Vista_Win7_6.0.1.6299_PV |
| LAN Intel(R) 82579V Gigabit Network Connection | Windows 7 | V11.8.84.0 | LAN_allIOS_11.8.84.0_P V |
| | Windows 7 64-bit | V11.8.84.0 | LAN_allIOS_11.8.84.0_P V |
| MEI Intel® Management Engine Interface | Windows 7 | V7.0.0.1144 | MEI_allIOS_7.0.4.1197_PV |
| | Windows 7 64-bit | V7.0.0.1144 | MEI_allIOS_7.0.4.1197_PV |
| USB 3.0 Renesas Electronics USB 3.0 Host Controller | Windows 7 | V2.0.26.0 | USB3_allIOS_2.0.26.0_P V |
| | Windows 7 64-bit | V2.0.26.0 | USB3_allIOS_2.0.26.0_P V |

Windows Logo Kits Used (WLK)

Microsoft website: <http://www.microsoft.com/whdc/DevTools/WDK/DTM.msp>

Please check regularly for test kit updates from Microsoft. Please ensure latest filters updated prior to WHQL run.

| Operating Systems | Notes | WHQL Testkit |
|-------------------------------|--------------------------------------|--------------------------------------|
| Windows 7 Windows 7 64-bit | WLK1.5 for Windows 7 | WLK1.5 for Windows 7 |

Errata and Contingencies

| Operating System | Failing Test | Expiry Date | ID Number | Type | Error Description |
|----------------------------|--|-------------|-----------|---------|--|
| Windows7 Windows7 64bit | Class Driver AC3 Test - Win7 (System) | 6/30/2025 | 1256 | Erratum | Run AC3 test on a system with the Microsoft HD Audio class driver installed. Expected results: All AC3 kernel streaming data ranges should advertise MinimumBitsPerSample = 16 and MaximumBitsPerSample = 16. Actual results: HD Audio class driver sometimes advertises MaximumBitsPerSample = 24. |
| Windows7 Windows7 64bit | PCI Hardware Compliance Test For Systems | 12/1/2012 | 401 | Erratum | The following PCI Compliance test failure is acceptable: Bit 15 (Bridge Configuration Retry Enable) in the Device Control register (offset 8h) in the PCI Express Capability table must be read-only and always return 0 as it is reserved for devices other than PCI Express to PCI/PCI-X Bridges. Assertion 13A41D3E-2576-41DC-A67C-525DA3637CEA This failure is acceptable because this is a PCIe 1.1 feature and the WLP requires compliance with only PCIe 1.0a. |
| | | 6/1/2011 | 423 | Erratum | Assertion 5CAF4993-B8D1-4E4E-99EC-CC5895364E32 Bit range 11:10 (ASPM Support)in the Link Capabilities register (offset Ch) in the PCI Express Capability table is 0h. It cannot be in the set of values {0x0, 0x2}. |
| | | 12/31/2011 | 2051 | Erratum | PCIHCT - Bit range 11:10 (ASPM Support)in the Link Capabilities register (offset Ch) in the PCI Express Capability table is 0h. It cannot be in the set of values {0x0, 0x2}. (Assertion 5CAF4993-B8D1-4E4E-99EC-CC5895364E32) |
| | | 12/1/2012 | 1241 | Erratum | This happens because the PCI Compliance test assumes that if the Data Link Layer Link Active Reporting Capable bit in the Link Capabilities register for a given PCIe port is set then that indicates that the Data Link Layer Link Active bit will also be set. This is an incorrect assumption because the Data Link Layer Link Active bit can be reset when there is no device below the port. This assertion needs to be removed from the PCIHCT. The current architecture of the PCIHCT prevents it from knowing whether devices exist below a bridge/port. |
| | | 12/1/2012 | 923 | Erratum | Assertion FAE18121-9177-4FB2-A081-0D04C285EFF2 Bit range 15:0 (Extended Capability ID)in the Enhanced Capability Header register (offset 0h) in the Unrecognized Enhanced Capability ID 13 table is Dh. It must be in the range [0x0 - 0xB] as all other Capability IDs are reserved. |
| Windows7 Windows7 64bit | UAA Test - Win7 (System) | 6/1/2015 | 513 | Erratum | UAA Test requires the Traffic Priority bit to be read/write - however there are two specs that apply, and they conflict. One says the bit must be read/write, the other says it must be read-only. Contact has been made with the author of both specs (Intel) but until this point is clarified we cannot fail submissions containing this test failure. |

Test Notes

| Operating System | Test | Description |
|---------------------------------------|---------------|--|
| Windows 7 | BIOS download | Internal: http://bios.intel.com/downloads/ External: http://www.intel.com/ click on Support and Download |
| Windows 7 | BIOS setup | Please make sure the BIOS setting are as below, otherwise use default settings. System Date and Time: Current date and time Peripheral Configuration: Enable all onboard component Drive Configuration: Set to AHCI Chipset Configuration: Enable HPET ACPI Suspend State: Set to <S3 State> Boot Device Priority: set <Hard Disk Driver> to first |
| Windows 7 filter update | WLK WHQL test | http://winqual.microsoft.com/member/SubmissionWizard/LegalExemptions/filterupdates.cab |
| Special H/W that use to PASS the test | None | None |