Intel® Server Board SE7320SP2
Intel® Server Board SE7525GP2

Specification Update

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Revision History

Date	Modifications
August 2004	Initial release based off of the "SE7320SP2 / SE7525GP2 Server Board Technical Product Specification".
November 2004	Errata: Updated items 1, 2 and added 9-16. Documentation: updated items 1-5 and added 6-7.
January 2005	BMC time stamp erratum; Event log full message displayed.
February 2005	CPU thermal trip in SEL.
May 2005	Errata: Updated items 5, 14, 15, 17, 20 and added items 21-23.
	Documentation: Added item 10.
June 2005	Errata: Updated items 13, 17, 21.
	Documentation: Updated item 10.
September 2005	Errata: Updated items 15, 21.
	Documentation: Updated items 9, 10.
October 2005	Errata: Updated items 15, 21 - change FRU/SDR release version; Updated item 19 - function enhanced.
	Documentation: Updated items 8, 9.

Disclaimers

The Intel® Server Boards SE7320SP2 and SE7525GP2 may contain design defects or errors known as errata that may cause the product to deviate from the published specifications. Current characterized errata are documented in this Specification Update.

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Preface

This document is an update to the product definition specified in the *Intel® Server Board SE7320SP2 and Intel® Server Board SE7525GP2 Technical Product Specifications* (Order Number C86768-002). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain specification changes, specification clarifications, errata, and document changes.

Refer to the *Intel® Xeon™ Processor Specification Update* (Document Number 249678-029) for specification updates concerning the Xeon™ processor. Items contained in the *Xeon™ Processor Specification Update* that either do not apply to the SE7320SP2 and SE7525GP2 Server Board or have been worked around are noted in this document. Otherwise, it should be assumed that any processor errata for a given stepping are applicable to the Printed Board Assembly (PBA) revisions(s) associated with that stepping.

Nomenclature

- **Specification Changes** are modifications to the current published specifications for the SE7320SP2 and/or SE7525GP2 Server Board. These changes will be incorporated in a future release of the given document.

- **Specification Clarifications** describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in a future release of the given document.

- **Documentation Changes** include typos, errors, or omissions from documents that are currently published. These documents may include Product Specs and Users Guides. These changes will be incorporated in a future release of the given document.

- **Errata** are design defects or errors. Errata may cause the SE7320SP2 and/or SE7525GP2 Server board's behavior to deviate from published specifications. Hardware and software designed to be used with any given processor stepping must assume that all errata documented for that processor stepping are present on all devices.

Product Scope

Below are the specific boards, BIOS and components covered by this update.

Baseboard Fab #	Baseboard PBA #	BIOS Revision / Build #	BMC Revision	FRU/SDR
Fab 6	C49813-601	P03	2.31	1.10
Fab 6	C49813-603 C55824-603	P05	2.40	1.30
Fab 6	C49813-603 C55824-603	P07	2.40	1.47
Fab 6	C49813-603 C55824-603	P08	2.40	1.50
Fab 6	C49813-604 C55824-604	P08	2.40	1.50
Fab 6	C49813-604 C55824-604	P08	2.40	1.60
Fab 6	C49813-604 C55824-604	P09	2.40	1.70

Summary Tables of Changes

The following tables indicate the errata and the document changes that apply to the Intel® Server Board SE7320SP2 and/or Intel® Server Board SE7525GP2. Intel intends to fix some of the specified errata in future updates to the server board and/or chassis. Documentation changes will be made in future updates to the given document. The tables use the following notations:

Doc: Intel intends to update the appropriate document in a future revision.

Investigating   Intel is investigating the issue.

Fix: Intel intends to fix this erratum in a future update of the board or chassis.

Fixed: This erratum has been addressed.

NoFix: There are no plans to fix this erratum.

Shaded: This erratum is either new or has been modified from the previous specification update.

Table 1. Errata Summary

No.	Plans	Description of Errata
1.	Fixed	Yellow ! (bang) appears in Microsoft* Windows* device manager over on board video when add-in video card is installed
2.	Fixed	"Memory Retest" option in BIOS setup is not getting disabled after a reboot.
3.	Fixed	Password mismatch errors cannot be cleared using Syscfg utiltiy
4.	No Fix	Dual monitor can be enabled when no add-in card is present
5.	Investigate	Floppy LED always lit in EFI shell
6.	Fixed	Slot 1 on board labeled as 'RADIOS' enabled when it is not
7.	Investigate	BIOS setup requires that a PCI add-in card be installed before option ROMs can be enabled or disabled
8.	No Fix	Potential data corruption manifested under SuSE* linux 9.1 professional operating system
9.	Fixed	Incorrect processor speed shown in SETUP, SMBIOS, POST, Windows
10.	Fixed	"INT 0000006F, Unknown Interrupt ---Halt" error showing up during POST
11.	Fixed	Int 15h command does not show proper memory amount
12.	Fixed	"PCI Onboard Unknown Device" displayed during POST
13.	Fixed	Can't use PCI-E x16 and onboard SATA RAID together
14.	Fixed	Illegal 3 DIMM configuration does not create beep code
15.	Fixed	System status LED does not turn amber when side cover removed
16.	Fixed	LSI Logic* 150-6 SATA RAID card causing system to hang during POST with BIOS P05 or P06
17.	Fixed	mBMC Timestamp Displayed Incorrectly
18.	No Fix	BIOS will display a warning message when the mBMC System Event Log (SEL) is full
19.	Fixed	CPU "Thermal Trip" messages in SEL and amber light on system front panel

20.	Fixed	Intel® Server Board SE7320SP2 and Intel® Server Board SE7525GP2 can not power on with Intel® Server Chassis SC5300BASE power supply
21.	Fixed	Incorrect ISM SYS Fan 2 message of Intel® Server Board SE7320SP2 and Intel® Server Board SE7525GP2 in Intel® Entry Server Chassis SC5275-E
22.	Fixed	Incorrect "MBMC.SENSOR_CHECK_VOLTAGE FAILED" message appears during PCT V1.02 continuous loop of Comprehensive Test

Table 2. Documentation Changes

No.	Plans	Description of Documentation Change
1.	Fixed	Section 3.1.6.1 of the TPS incorrectly refers to the IMM (Intel Management Module) upgrade which the SE7320SP 2 does not support.
2.	Fixed	Section 4.9.3 of the TPS incorrectly states that SE7320SP2 supports a sleep button on the chassis front panel
3.	Fixed	Table 74 in TPS incorrectly identifies the header location as J30 when it should be J54
4.	Fixed	Descriptions for J26 and J56 in TPS are missing
5.	Fixed	*Intel® Server Board SE7320SP2 and Intel® Server Board SE7525GP2 Technical Product Specification* mentions the SE7320SP2LX SKU which has been re-defined.
6.	Fixed	Boxed board label incorrectly identifies Fan 3 and Fan 4 on baseboard
7.	Fix	Reference to power supply minimum wattage in User Guide is mis-leading.
8.	Fixed	Front Panel pin-out is incorrect in TPS
9.	Fixed	WOL is not supported from S5 as mentioend in TPS
10.	Fixed	*Intel® Server Platform SC5275GP2 Quick Start Guide* mislabels DIMM bank 1 as DIMM bank 2 and vice versa

Following are in-depth descriptions of each erratum / documentation change indicated in the tables above. The errata and documentation change numbers below correspond to the numbers in the tables.

Errata

1. Yellow ! (bang) appears in Microsoft* Windows* device manager over on board video when add-in video card is installed

Problem   When an add-in video card is placed in the system, the BIOS does not completely disable the onboard video device. Windows places a yellow exclaimation point (commonly referred to as a 'bang') over the device in device manager.

Implication   There is no functionality problem. Primary video is still directed to the add-in video controller. Customers may notice this as call for support.

Workaround   Manually disable the on-board controller in BIOS setup when installing an add-in adapter.

Status   Fixed in BIOS P05 release.

2. "Memory Retest" Option in BIOS setup is not getting disabled after a reboot.

Problem   If a user sets the "Retest Memory" flag in BIOS Setup to Enabled, this should reset itself to "Disabled" at the next reboot. This is not happening.

Implication   The server board will force a retest of all the memory at each reboot.

Workaround   Re-enter BIOS setup and manually disable "Memory Reset" upon the next
                   reboot.

Status   Fixed in BIOS P06 release

3. Password mismatch errors cannot be cleared using Syscfg utility

Problem   Entering an incorrect password three times using the Syscfg utility will render
                   the utility useless from ever being able to set the password again.

Implication   System administrators would not be able to use the Syscfg utility to manage
                   BIOS settings on the system.

Workaround   Use password reset jumper to reset the password and then use BIOS setup to
                   set and change passwords.

Status   Fixed in the -003 revision of the Intel® Server Board SE7320SP2 and Intel®
                   Server Board SE7525GP2 Resource CD.

4. Dual Monitor can be enabled when no add-in card is present

Problem   A user can enter BIOS setup and set up dual monitor mode when no add-in
                   video card is present in the system.

Implication   Setting dual monitor mode with out an add-in card in the system will cause the
                   on-board video to not display anything. This could cause customer confusion
                   and support calls.

Workaround   Ensure Dual Monitor mode is Disabled if no add-in video adapters are present
                   in the system.

Status   This issue will not be fixed.

5. Floppy LED is always lit in EFI shell

Problem   When the system is booted to the EFI shell, the floppy LED comes on and
                   stays on.

Implication   Customers may become confused as this is not normal operation under an
                   operating system.

Workaround   Currently there is no work around for this issue. The LED being on does not
                   affect reads from, or writes to, the drive.

Status   Intel is investigating a fix for the issue in a future BIOS release.

6. Slot 1 on the board is labeled as 'RADIOS' enabled when it is not.

Problem   The silkscreen on the baseboard indicates that Slot 1 (closest to the edge of
                   the board) is RADIOS enabled. This is not the case.

Implication	Customers may become confused if they are trying to implement a RADIOS enabled RAID design on this system.
Workaround	None
Status	This has been resolved on all boards with PBA revision -603 and higher.

7. BIOS setup requires that a PCI add-in card be installed before option ROMs can be enabled or disabled

Problem	The option to disable the Option ROM for a specific card in a specific slot on the board will not be visible in BIOS Setup unless a card is installed in the system.
Implication	This may affect troubleshooting of the system if a hang is encountered during POST that appears to be related to a particular card or option ROM as BIOS Setup cannot be entered until after all Option ROMs have loaded.
Workaround	Users will need to manually add and remove cards to perform their troubleshooting.
Status	Intel is investigating a fix in a future release of the BIOS.

8. Potential data corruption manifested under SuSE* linux 9.1 professional operating system

Problem	Intel found through OS stress testing that the IA32 and the IA32e versions of the SuSE Linux 9.1 Professional operating system (kernel 2.6.4-52-smp) can exhibit silent data corruption on server boards based on the Intel(R) E7520 chipset.
Implication	Investigation determined the data corruption stemmed from a race condition in reiser file system that ships with SuSE Linux 9.1 Professional OS (2.6.4-based kernel). Although the reiser file system is the default file system used by SuSE Linux; tests of alternative file systems supported by the OS have not manifested this data corruption. All investigations and test results ascertain that this anomaly only affects the IA32 and IA32e versions of the SuSE* Linux 9.1 Professional OS. This anomaly is expected to be addressed by the OS vendor on the SuSE Linux Enterprise Server 9.0 version when officially released in early August 2004. SuSE Linux 9.1 Professional version will not be stress tested nor supported on the Intel® E7320 / Intel® E7525 server platforms.
Workaround	None supported by Intel.
Status	This issue will not be fixed.

9. Incorrect processor speed shown in SETUP, SMBIOS, POST, Windows

Problem	Processor speed is being reported as slower than actually installed in the system in most cases. This is a result of an incorrectly calculated bus/core

ratio which gets set when the first processor is installed and then does not get updated when a newer, faster processor is installed.

Implication   System may perform below expectations. Users may notice incorrect processor speed shown in Operating System or BIOS setup.

Workaround   None

Status   This issue is resolved in the BIOS release P06

10.   "INT 0000006F, Unknown Interrupt ---Halt" error showing up during POST

Problem   The error message "INT 0000006F, Unknown Interrupt ---Halt" appears on the screen just prior to booting into the EFI shell. This error message is a result of a spurrious interrupt being generated by the Intel® 6300ESB prior to loading the EFI shell. The shell does not know what this interrupt is, so it displayes the error.

Implication   This error has been determined to be a false error and no system halt or error occurs as a result of this.

Workaround   Do not boot to EFI shell

Status   This issue is resolved in the BIOS release P06

11.   Int 15h command does not show proper memory amount

Problem   Int15h, E801h function is not returning proper information in the AX, BX, CX and DX registers (for memory configuration 3GB or above). The AX and CX should always have a value of 3C00h, which represents 15MB of memory. AX = Memory installed between 1MB and 16MB in kb units, CX = Memory configured between 1MB and 16MB in kb units. The BX and DX registers will have different values depending on how much total memory is installed. BX = Memory installed above 16MB in 64kb units, DX = Memory configured above 16MB in 64kb units.

Implication   In OSes that do not have access to above 4GB of memory, an incorrect memory amount will be shown by the operating system.

Workaround   None that Intel supports

Status   This issue is resolved in the BIOS release P06

12.   "PCI Onboard Unknown Device" displayed during POST

Problem   If the user has Quick Boot disabled, Quiet Boot disabled and POST error paus enabled, then an installed device summary table will appear prior to the OS loading. In this table, the first item shows up as "PCI Onboard Unknown Device".

Implication   No impact to the functionality of the system

Workaround   None

Status   This issue is resolved in the BIOS release P06

13. Can't use PCI-E x16 and onboard SATA RAID together

Problem   When certain PCI-Express x16 graphics adapters are installed in the system, the onboard SATA Option ROM will not load. This is due to a limitation in the system BIOS Option ROM space.

Implication   Certain PCI-Express x16 graphics adapters and the onboard SATA RAID capability cannot be used together in the system.

Workaround   In order to get the SATA RAID to work, a different PCI-Express graphics card must be used or the PCI-Express graphics adapter must be removed from the system.

Status   This issue has been fixed in the BIOS release P08.

14. Illegal 3 DIMM configuration does not create beep code

Problem   When 3 DIMMs are populated, the entire DIMM size is not reported and no beep codes are generated to let the user know this is an illegal memory configuration. Per the Technical Product Specification, only 1, 2 and 4 DIMM configurations are allowed on the SE7320SP2 and SE7525GP2 server boards.

Implication   Users will not be alerted by the system that an illegal memory configuration is installed. Memory capacity will not match what is installed in the system.

Workaround   Use only a supported memory configuration.

Status   This issue is resolved in BIOS release P07, by creating a beep code to alert users to this illegal configuration

15. System status LED does not turn amber when side cover removed

Problem   When the side cover is removed from the system, the system status LED does not turn amber to alert the user to a minor system failure.

Implication   User may not know via LED alert that something has happened on the system. This event is still logged to the SEL, however.

Workaround   None at this time.

Status   This one has benn fixed with FRU/SDR v1.60 release.

16. LSI Logic* 150-6 SATA RAID card causing system to hang during POST with BIOS P05 or P06

Problem   The LSI 150-6 RAID controller will cause the system to hang when BIOS P05 or P06 is installed in the system. Several enhancements were made to BIOS P05 which now conflict with memory space used by the LSI controller.

Implication   Users will not be able to use this controller in the system with the P05 or P06 BIOS installed.

Workaround   Flash the system BIOS back to version P04.

Status   This issue is resolved in the LSI firmware release 713N.

17.   mBMC Timestamp Displayed Incorrectly

Problem   The Intel® Server Board SE7320SP2 / SE7525GP2 have been found to have a BIOS erratum which causes the mBMC timestamp information to be incorrect. Beginning on January 1, 2005, the mBMC date will lag the system date by 1 day. The system date January 1, 2005 will appear as December 31, 2004 in the mBMC, and the mBMC will be one day behind thereafter. Another day will be lost on January 1 of each succeeding year that follows a leap year, i.e. 2009, 2013, etc.

Implication   The effect of this erratum is that the mBMC will use this incorrect date for all entries in the System Event Log (SEL) maintained by the mBMC. This includes informational events as well as error events, e.g. memory error events. Other mBMC functions are unaffected.

Workaround   None

Status   This issue has been fixed in BIOS release P08.

18.   BIOS will display a warning message when the mBMC System Event Log (SEL) is full

Problem   During boot, the BIOS may briefly display a red warning message indicating that the System Event Log (SEL) is full. This message appears after multiple reboots, due to the limited SEL storage space of the On-Board Platform instrumentation. The SEL storage space of the mBMC allows for the storage of 92 SEL entries. A typical reboot will add several informational event messages to the SEL as part of the normal boot process.

Implication   The red warning message displayed by the BIOS is not an error. It is a warning message that the SEL is full and that no more system event messages can be logged until the SEL is cleared.

Workaround   The System Event Log (SEL) of the On-Board Platform Instrumentation should be cleared regularly. There are several methods of clearing the SEL, including; BIOS (F2) Setup, SEL Viewer (included on the Intel Server Deployment Toolkit), and Intel Server Management 8.x.   Intel Server Management 8.x also includes the capability to manage the SEL and can be configured to automatically clear the SEL when it reaches a user defined % full.

Status   Intel has no plans to increase the SEL capacity of the On-Board Platform Instrumentation beyond the current 92 entries. Intel is investigating adding a BIOS (F2) Setup option to allow a user to limit the informational boot events logged in the SEL. Information on this BIOS (F2) Setup option will be included in future Spec Updates when implemented.

19. CPU "Thermal Trip" message in SEL and amber light on system front panel

Problem   CPU "Thermal Trip" messages (one for each processor) may be logged in the mBMC SEL after system is shutdown using either the Operating System mechanism or via the front panel power button. The system status LED will also turn amber and remain amber until the power button is reactivated. Also, if the power button is reactivated immediately following the shutdown the system may fail to POST and shutdown momentarily.

Implication   The "Thermal Trip" message logged in the mBMC SEL would indicate that the processors are exceeding defined thermal thresholds. However, measurements conducted at Intel indicate that the processor thermal thresholds are not being exceeded.

Workaround   A new version of FRU/SDR, V1.4E, which removes the CPU "Thermal Trip" sensor, is available for use as an interim solution only.

Status   Have function enhanced with FRU/SDR V1.70 release to fit for case that is more rigorous.

20. Intel® Server Board SE7320SP2 and Intel® Server Board SE7525GP2 can not power on with Intel® Server Chassis SC5300BASE power supply

Problem   When installing Intel® Server Board SE7320SP2 / SE7525GP2 with an Intel® Server Chassis SC5300 (SC5300 Base SKU only) Power Supply, the server system fails to boot up.

Implication   Intel® Server Board SE7320SP2 and/or SE7525GP2 cannot be powered on by the C44675-007 Power Supply, which is shipped together with Intel® Server Chassis SC5300 (SC5300BASE SKU only), for the minimum loading condition of the power supply has not been met.

Workaround   None

Status   This issue has been fixed with FRU/SDR V1.47 release.

21. Incorrect ISM SYS Fan 2 message of Intel® Server Board SE7320SP2 and Intel® Server Board SE7525GP2 in Intel® Entry Server Chassis SC5275-E

Problem   ISM reports *"Fan SYS Fan2: low critical"* and/or *"Fan SYS Fan2: low non-critical"* event continually and randomly in Microsoft* Windows*, while the system fan 2 is actually normal working.

Implication   This error message has been determined to be a false one, based on measurements conducted at Intel.

Workaround   Increasing the fan speed or decreasing the fan thresholds are potential solutions to this problem.

Status   It has been fixed with FRU/SDR v1.60 release.

22. Incorrect "MBMC.SENSOR_CHECK_VOLTAGE FAILED" message appears during PCT V1.02 continuous loop of Comprehensive Test

Problem   This error message only appears while running PCT (Platform Confidence Test) V1.02 continuous loop of Comprehensive Test.

Implication   This error message has been determined to be a false one, based on measurements conducted at Intel.

Workaround   None

Status   This issue has been fixed in PCT Release V1.03.

Documentation Changes

1. Section 3.1.6.1 of the TPS incorrectly refers to the IMM (Intel Management Module) upgrade which the SE7320SP2 does not support.

Problem	The last paragraph in section 3.1.6.1 mentions the use of the Intel Management Module (Professional or Advanced) upgrade in reference to the Intel® Server Soard SE7320SP2. The board does not have have the capability of using this management module. This information was erroneously included in this section of the documents.
Implication	The functionality that is described in this section in relation to the IMM is not possible on this board. If this functionality is required, a different Intel server board will need to be purchased.
Status	Fixed in release 2.0 of the Technical Product Specification

2. Section 4.9.3 of the TPS incorrectly states that SE7320SP2 supports a sleep button on the chassis front panel

Problem	The second paragraph in section 4.9.3 of the *Intel® Server Board SE7320SP2 and Intel® Server Board SE7525GP2 Technical Product Specification* mentions the system supports a front panel sleep button. This functionality is no longer part of the design of this product and therefore the statement made in the TPS is inaccurate.
Implication	Sleep states will need to be configured through the power management functionality within each operating system. There is no hardware button to force the system to sleep.
Status	Fixed in the 2.0 release of the Technical Product Specification

3. Table 74 in TPS incorrectly identifies the header location as J30 when it should be J54

Problem	The title of Table 74 (HSBP Header Pin-out) incorrectly identifies the signals of this header to be located at J30 on the baseboard when, in fact, this header is located at J54.
Implication	The location J30 is not stuffed on the board so therefore the functionality is not available at this location.
Status	Fixed in the 2.0 release of the Technical Product Specification

4. Descriptions for J26 and J56 in TPS are missing

Problem   The signal pin-outs for header J26 and J56 are missing in the TPS

Implication   Exact signaling for these signals may not be known and may impact cable or peripheral design.

Status   Fixed in the 2.0 release of the Technical Product Specificaiton

5. *Intel Server Board SE7320SP2 / SE7525GP2 Technical Product Specification* mentions the SE7320SP2LX SKU which has been re-defined.

Problem   The *Intel® Server Board SE7320SP2 and Intel® Server Board SE7525GP2 Technical Product Specification* discusses the SE7320SP2LX server board SKU throughout the document. This version of the SE7320SP2 server board has been postponed and redefined since the original publication of this document.

Implication   The product and featureset mentioned in the *Intel® Server Board SE7320SP2 / SE7525GP2 Technical Product Specification* for the SE7320SP2LX SKU is not accurate and will be changed.

Status   Fixed in the 2.0 release of the Technical Product Spefication

6. SYS_FAN 3 and SYS_FAN 4 incorrectly identified on Boxed Board label.

Problem   The label that shipps with the boxed board SE7320SP2 and SE7525GP2 incorrectly identifies SYS_FAN 3 and SYS_FAN 4. These two headers are shown swapped on the label.

Implication   The System Status LED will be illuminated amber in the Intel chassis if the fans are plugged into the incorrect header.

Status   Fixed. This issue has been resolved on the newest version of this label. Labels with part number C62034-003 or greater have the correct fan labeling.

7. Reference to power supply minimum wattage in User Guide is mis-leading

Problem   The User Guide states that a 600W power supply is the minimum level supported by the baseboard. This measurement is based on a fully loaded SE7525GP2 server board with a 150W PCI-E x16 graphics card populated. A lesser configuration can be supported by a smaller power supply.

Implication   There is no issue with using a larger power supply with the system.

Status   The User Guide is being updated to reflect that 600W is recommended for a heavily loaded system, however the user should perform their own power budgeting to determine the power supply size most appropriate for their application and configuration.

8. Front Panel pin-out incorrect in TPS

Problem   The Technical Product Specification (TPS) incorrectly defines the pin-outs for the Intel® Server Board SE7320SP2 and SE7525GP2. The TPS documents pins 12 &14 as being NIC #2 Anode and Cathode respectively and pins 22 & 24 as NIC #1 Anode and Cathode respectively. These are actually reversed. Pins 12 & 14 should be labeled as NIC #1 Anode and Cathode respectively and Pins 22 & 24 should be labeled as NIC #2 Anode and Cathode respectively.

Implication   In Intel chassis, the front panel is connected via a flat ribbon cable, so there is no impact. However in 3rd party chassis, incorrectly hooking up the NIC sigals could cause incorrect functionality on the front panel.

Status   It has been fixed with TPS ver 3.0 release.

9. WOL not supported from S5 as mentioned in the TPS

Problem   The TPS incorrectly mentions that the Intel® Sever Board SE7525GP2 and SE7320SP2 can be awakened from S5 by WOL. This is not the case. All E7320 and E7525 based products can only be woken from S1 or S4. WOL is not supported from S5.

Implication   System administrators who utilize WOL to manage server availability may incorrectly configure their systems.

Status   We have had WOL from S5 supported with BIOS P09 release, and updated WOL from S5 introduction on TPS ver 3.0.

10. *Intel® Server Platform SC5275GP2 Quick Start Guide* mislabels DIMM bank 1 as DIMM bank 2 and vice versa

Problem   This Quick Start Guide (Order Number C89562-001) contains incorrect references to DIMM bank 1 and DIMM bank 2 location.

Implication   Be certain to install memory to the right slot per the identifier printed on PCB.

Status   It was correced in the new revision of Quick Start Guide, the order number is PNC89563-002.