



SBT2 Server Board

Specification Update

Intel Order Number A44371-011

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Enterprise Platforms and Services Marketing



Revision History

Date	Revision Number	Modifications
October, 2000	001	Initial release.
November, 2000	002	No updates. Changed order numbers.
December, 2000	003	Changed copyright information. Corrected header to SBT2 from STL2 Added Doc errata 1 and 2 Updated erratum 7 Changed status of erratum 9 from Fix to Fixed Changed status of erratum 12 from Fix to Fixed Added erratum 15 – 17
January, 2001	004	Changed copyright information Added erratum 18
February – April, 2001		No updates
May, 2001	005	Changed erratum 3 Added errata 18 – 25
June, 2001	006	No updates
July, 2001	007	Added BIOS 1.10
August, 2001	008	Changed format to be more consistent with other documentation
September 01	009	Added erratum 26
October 01	010	No changes
November 01	011	Added Erratum 27
December 01	012	No changes
January 02	013	No Changes
February 02	014	No Changes
March 02	015	No Changes
April 02	016	No Changes
May 02	017	No Changes
July 2002	018	No Changes or new Errata
September 2002	19	No Changes or new Errata

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The SBT2 Server Board may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

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Preface

This document is an update to the specifications contained in the *SBT2 Server Board Technical Product Specification* (Order Number [TPS order number]). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain specification changes, specification clarifications, errata, and document changes.

Refer to the *Intel® Pentium® III Xeon™ Processor Specification Update* (Order Number 244460-021) for specification updates concerning the Pentium® Xeon™ III processor. Items contained in the *Pentium® III Xeon™ Processor Specification Update* that either do not apply to the [product] or have been worked around are noted in this document. Otherwise, it should be assumed that any processor errata for a given stepping are applicable to the Printed Board Assembly (PBA) revisions(s) associated with that stepping.

Nomenclature

- **Specification Changes** are modifications to the current published specifications for the SBT2 server boards. These changes will be incorporated in the next release of the specifications.
- **Specification Clarifications** describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.
- **Documentation Changes** include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.
- **Errata** are design defects or errors. Errata may cause the SBT2 server board's behavior to deviate from published specifications. Hardware and software designed to be used with any given processor stepping must assume that all errata documented for that processor stepping are present on all devices.

Product Scope

Below are the specific boards, BIOS and components covered by this specification.

Baseboard Fab #	Baseboard PBA #	BIOS	SSU	Processor Stepping	Chipset Stepping (ServerWorks* ServerSet* III LE CNB30LE & ROSB4)
1	A28258-100	Release 1.1	Release 1R1	Pentium® III Xeon™ processor: B0, C0	RCC-NB6635-P02(2.2) RCC-IB6566-P03(A4.0)
1	A28258-101	Release 1.6	Release 2R1	Pentium® III Xeon™ processor: B0, C0	RCC-NB6635-P03(2.3) RCC-IB6566-P04(B1.0)
1	A28258-101	Release 1.10	Release 2R1	Pentium® III Xeon™ processor: B0, C0	RCC-NB6635-P03(2.3) RCC-IB6566-P04(B1.0)

Summary Tables of Changes

The following tables indicate the errata and the document changes that apply to the SBT2 Server Board. Intel intends to fix some of the errata in a future stepping of components, and to account for the other outstanding issues through documentation or specification changes as noted. The tables use the following notations:

Doc: Intel intends to update the appropriate documentation in a future revision.

Fix: Intel intends to fix this erratum in a future release of the component.

Fixed: This erratum has been previously fixed.

NoFix: There are no plans to fix this erratum.

Shaded: This erratum is either new or has been modified from the previous specification update.

Table 1. Errata Summary

No.	Plans	Description of Errata
1.	NoFix	4GB memory size reported incorrectly during POST
2.	NoFix	Arrowhead card fails installation under Microsoft* Windows 2000*
3.	NoFix	DOS load fails with Fujitsu* IDE hard drive model MPE3084AE
4.	Fixed	BIOS update process does not ask for confirmation
5.	NoFix	BMC firmware update process power down the system automatically upon completion
6.	NoFix	BMC firmware corruption is a non-recoverable condition
7.	NoFix	PIO IDE mode 3 drives cause no boot condition
8.	Fixed	SC5000 350 watt power supply fan failure not reported in the SEL
9.	Fixed	Boot order issue with SCSI CDROM drives
10.	Fixed	Three percent no boot failure following battery replacement due to Super I/O errata
11.	Fixed	RedHat* Linux* 6.1 installation issue
12.	Fixed	Processor Errors during POST following quick system power cycling
13.	NoFix	SC5000 chassis HSC firmware update is not possible with the SBT2 server board installed
14.	NoFix	The SBT2/SC5000 network activity, hard drive activity, and fault front panel LEDs do not work
15.	NoFix	Power LED does not blink when system in ACPI Standby mode
16.	Fixed	Failing DIMM location not shown in SSU
17.	Fix	RedHat* Linux* 6.2 SBE2 and 7.0 only recognizing 64MB system RAM with BIOS Releases 1.5 and 1.6
18.	Fixed	Firmware update process prompts for an 'ESC' key before updating
19.	Fixed	Microsoft* Windows* 2000 installation issue with USB enabled in BIOS Setup
20.	Fixed	Powering on without memory installed causes processor POST error
21.	Fixed	Powering on without memory installed causes processor POST error
22.	NoFix	Certain PS/2 Keyboards are not detected by BIOS or cause no video co

23.	Fixed	BMC Firmware Update Utility Anomalies
24.	Fixed	TCO Packet Processing Issue
25.	Fixed	BIOS containing Microcode Update Fix for 933MHz B stepping Pentium® III Xeon™ Processor Erratum
26.	NoFix	Incorrect hard drive capacity may be displayed during POST
27.	NoFix	Anomaly when upgrading SBT2 BMC Firmware

Table 2. Documentation Changes

No.	Plans	Description of Documentation Change
1.	Fix	Quick Start Guide processor changes
2.	Fix	Product Guide processor changes

Following are in-depth descriptions of each erratum / documentation change indicated in the tables above. The errata and documentation change numbers below correspond to the numbers in the tables.

Errata

1. 4 GB memory size reported incorrectly during POST

Problem	When 4 GB total memory is installed in the SBT2 server board, the BIOS reports the extended memory size as 3,999 MB during POST. The expected extended memory size is 4095 MB. The OS can access all of the installed memory.
Implication	The BIOS will report the extended memory size as 3,999 MB during POST, when 4 GB total memory is installed in the SBT2 server board. The OS can access all of the installed memory. Therefore, this issue has no impact on product functionality.
Workaround	A workaround does not exist for this issue.
Status	No Fix. This issue is caused by an erratum in the ServerWorks* III LE chipset.

2. Arrowhead card fails installation under Microsoft* Windows 2000*

Problem	The Arrowhead server management card will not complete installation when used with the SBT2 server board and Microsoft* Windows* 2000. A black screen is encountered during the installation.
Implication	The Arrowhead server management card cannot be utilized with the SBT2 server board and Microsoft Windows 2000.
Workaround	No workaround exists for this issue.
Status	No Fix.

3. DOS load fails with Fujitsu* IDE hard drive, model MPE3084AE

Problem	DOS cannot be loaded to a Fujitsu* IDE hard drive, model MPE3084AE. The system hangs during the installation.
Implication	The Fujitsu IDE hard drive, model MPE3084AE cannot be utilized with the SBT2 server board.
Workaround	No workaround exists for this issue.
Status	No Fix.

4. BIOS update process does not ask for confirmation

Problem	When a SBT2 BIOS update is performed, the SBT2 BIOS update utility immediately begins programming the BIOS, upon boot from the BUIS update diskette. It does not prompt for confirmation first. This is different than the BIOS update process for other Intel® server products, which does prompt the user to confirm the BIOS update before proceeding.
Implication	The SBT2 BIOS updates will be performed immediately after booting to the BIOS update diskette, without user confirmations.
Workaround	No workaround exists for this issue.
Status	Fixed. This issue was fixed in SBT2 BIOS Release 1.5 (Build 16) and later versions.

5. BMC firmware update process powers down the system automatically upon completion

Problem	When a SBT2 BMC firmware update process is performed, the update utility automatically powers down the system upon successful completion, without first prompting for power-down confirmation. This is different from the BMC firmware update process for other Intel server products.
Implication	The SBT2 system will automatically power down when a BMC firmware update is successfully completed. This behavior is expected.
Workaround	No workaround exists for this issue.
Status	No Fix.

6. BMC firmware corruption is a non-recoverable condition

Problem	If a BMC firmware corruption occurs on a SBT2 board during the BMC firmware update process or by other means, this is a non-recoverable condition. This is different from some Intel server boards, which include a BMC force update jumper to allow for recovery from BMC firmware corruption.
Implication	Since BMC firmware corruption is a non-recoverable condition, extra care should be taken to ensure the SBT2 system is not accidentally powered down during a firmware update process.
Workaround	No workaround exists for this issue.
Status	No Fix.

7. PIO IDE mode-3 drives cause “no boot” condition

Problem	The SBT2 server board will not boot with a PIO IDE mode-3 drive connected. Only PIO mode 0 and 4 IDE drives are supported on SBT2 server boards.
Implication	PIO IDE mode-3 drives cannot be utilized with the SBT2 server board.
Workaround	No workaround exists for this issue.
Status	No Fix.

8. SC5000 350-watt power supply fan failure is not reported in the System Event Log

Problem	When an SBT2 server board is installed in the SC5000 chassis, a 350-watt power supply fan failure will not cause a System Event Log entry for this failure.
Implication	SC5000 chassis 350-watt power supply fan failures will not be recorded in the SBT2 server board System Event Log.
Workaround	No workaround exists for this issue.
Status	Fixed. This erratum has been fixed in SBT2 FRU/SDR Version 4.3.7.

9. Boot order issue with SCSI CD-ROM drives

Problem	The SBT2 server board changes the boot order of an installed SCSI CD-ROM drive when a CD-ROM disk is not loaded in the drive. The SCSI CD-ROM drive is moved to the bottom of the boot priority list in BIOS setup when the system is booted without a CD-ROM disk in the CD-ROM drive.
Implication	The customer may need to change the boot order of the SCSI CD-ROM drive in <F2> BIOS Setup before booting the system from a bootable CD-ROM disk.
Workaround	Set the boot order for the SCSI CD-ROM drive in <F2> BIOS Setup immediately before booting the system from a bootable CD-ROM disk.
Status	Fixed. This issue is fixed in BIOS Release 17, Build 18.

10. Three percent (3%) no-boot failure following battery replacement due to Super I/O errata

Problem	Due to an erratum with the Super I/O PC97317 component, approximately three percent of SBT2 server boards will not boot after replacing an expired battery.
Implication	When the battery on the SBT2 server board fails and is replaced, a 3% chance exists that the board will not boot.
Workaround	No workaround exists for this issue
Status	Fixed. This erratum is fixed in SBT2 BIOS Release 1.5 (Build 16) and later versions.

11. RedHat* Linux* 6.1 installation issue

Problem	The driver for the on-board Adaptec* AIC-7899 SCSI controller included with the RedHat* Linux* 6.1 distribution does not load during the first part of the installation. The following error message appears: "SCSI HOST 0 ABORT TIMED OUT - RESETTING".
Implication	Difficulties may be encountered when installing RedHat Linux 6.1 on the SBT2 server board.
Workaround	RedHat Linux 6.2 SBE2 includes the correct drivers to allow a normal installation. Install RedHat Linux 6.2 or update the Adaptec drivers in RedHat Linux 6.1
Status	Fixed. This erratum is fixed in RedHat Linux 6.2 SBE2.

12. Processor errors during POST follow quick power cycling

Problem	Processor errors may appear during POST following quick system power cycling. If the SBT2 server board is powered on and then powered off before the FRB-3 timer completes (about 10 seconds), the SBT2 BIOS does not stop the FRB-3 timer. In addition, the BMC firmware does not stop the FRB-3 timer automatically, so a FRB-3 timeout occurs, disabling the bootstrap processor (BSP). This causes a processor error to appear during POST when the system is next powered on.
Implication	Processor errors may appear during POST following quick system power cycling.
Workaround	The processor error can be cleared through <F2> BIOS Setup. Select Main / Processor / Clear Processor Errors. Press F10 and then Enter to Save Changes and Exit. The processor error should not appear on the next boot.
Status	Fixed. This erratum is fixed in BMC Firmware Release 1.17 and later.

13. SC5000 chassis HSC firmware update is not possible with the SBT2 server board installed

Problem	It is not possible to update the SC5000 chassis HSC firmware if the SBT2 server board is installed in the chassis. This is because the SBT2 server board does not support an I2C interface connection between the server board and the chassis hot-swap backplane / SAF-TE card. The universal versions of the SC5000 chassis include the latest version of the HSC firmware available. Any changes to the HSC firmware will be made by incorporating the ECO process.
Implication	It is not possible to update the HSC firmware in the SC5000 chassis if the SBT2 server board is installed in the chassis.
Workaround	It is possible to update the HSC firmware in the SC5000 chassis if the L440GX+ server board is installed. If a non-universal version of the SC5000 chassis requires an update to the HSC firmware, an L400GX+ board can be installed in the chassis to perform the HSC firmware update.
Status	No Fix.

14. SBT2 / SC5000 network activity, hard drive activity, and fault front panel LEDs do not work

Problem	When the SBT2 server board is installed in the SC5000 chassis, the network activity, hard drive activity, and fault front panel LEDs do not work. This is because there are no connections for network activity, hard drive activity, or fault LEDs on the SBT2 server board.
Implication	When the SBT2 server board is installed in the SC5000 chassis, the network activity, hard drive activity, and fault front panel LEDs will not function.
Workaround	Examine the System Event Log for indications of system faults.
Status	No Fix.

15. SBT2 Power LED does not blink when in ACPI System Standby mode

Problem	When the SBT2 server board is requested to enter ACPI Standby mode, the Power LED does not blink.
Implication	When the SBT2 server board is requested to enter ACPI Standby mode, the Power LED does not blink. This is typically on systems running Windows 2000.
Workaround	No workarounds exist for this issue.
Status	No Fix

16. SBT2 SSU does not show the location of a failing DIMM

Problem	If a SBT2 server board has a failing DIMM, the location is not reported in the System Setup Utility
Implication	If a DIMM is failing on the SBT2 server board, server control software cannot see the location of the failing DIMM.
Workaround	No workaround exists for this issue.
Status	Fixed. This erratum is fixed in SSU 2.R.1.

17. RedHat Linux 6.2 SBE2 and 7.0 recognize only 64 MB of memory when using BIOS Releases 1.5 and 1.6

Problem	SBT2 server boards programmed with BIOS Release 1.5 (Build 16) or BIOS Release 1.6 (Build 17) running RedHat Linux 6.2 SBE2 or 7.0 will recognize only 64 MB of the total installed memory, regardless of the actual amount of memory installed on the server board. Other operating systems will recognize the total installed memory.
Implication	RedHat Linux 6.2 SBE2 or 7.0 will recognize only 64 MB of the total installed memory.
Workaround	No workaround exists for this issue.
Status	Fix.

18. Firmware update utility prompts for an 'ESC' before updating firmware

Problem	When an attempt is made to update the SBT2 firmware, the user is prompted to press the 'ESC' key to get to the main firmware update utility menu.
Implication	This prompt may potentially confuse the customers.
Workaround	Press the 'ESC' key as prompted.
Status	Fixed. This issue is fixed in BMC Firmware V1.19 and future releases.

19. Microsoft Windows 2000 installation issue with USB enabled in BIOS Setup

Problem	Microsoft Windows 2000 cannot be installed on the SBT2 server board with BIOS Release 1.6 (Build 17) and previous versions if the USB controller is set
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	to “Enabled” in <F2> BIOS Setup. The SBT2 USB controller is set to “Disabled” by default.
Implication	If the SBT2 USB controller is set to “Enabled” in BIOS Setup, Windows 2000 will hang during installation.
Workaround	Windows 2000 will install on the SBT2 server board if the USB is set to “Disabled” in BIOS Setup.
Status	Fixed. This issue has been fixed in SBT2 BIOS Release 1.8 (Build 19) and later versions.

20. Powering the system on without memory installed causes a processor POST error

Problem	If the SBT2 server board is powered on without system memory installed, then is powered off, memory is installed, and the system is powered back on, the following error will appear during POST: <pre>Error 0B5F: Forced to use Processor with error. Press <F1> to resume, <F2> to Setup, <F12> to Network</pre>
Implication	If the SBT2 server board is powered on without system memory installed, the next time the system is powered on, a processor POST error will appear.
Workaround	Use <F2> BIOS Setup to select the option to clear processor errors.
Status	Fixed. This issue has been fixed in SBT2 BIOS Release 1.8 (Build 19) and later versions.

21. Front Panel NIC LED exhibits activity when the system is powered off.

Problem	When the SBT2 server board is powered off, the front panel NIC LEDs exhibit activity.
Implication	Intel® Server Control software will not detect a problem and the physical fault LED will not show that the power supply module has been removed or if it has failed.
Workaround	This is expected behavior.
Status	Fixed.

22. Some PS/2 keyboards are not detected by BIOS or cause a no video condition

Problem	PS/2 keyboards that utilize keyboard connector pins 2 or 6 may not be detected by the SBT2 BIOS, or may cause no video conditions when connected to the SBT2 server board. Keyboards that have been found to exhibit this issue are Samsung* Electics (SEM-A17K), Samsung Magic Keyboard (no model listed), Samsung (SEMA17S), and Fujitsu* (FKB8735-T201). These keyboards either are not compatible or they utilize keyboard connector pins 2 and 6.
Implications	The SBT2 server board will display "Keyboard Error" during BIOS post, or it will fail to display video.
Workaround	No workaround exists for this issue.
Status	No Fix.

23. BMC Firmware Update Utility anomalies reported

Problem / Implications

Several anomalies exist with the SBT2 BMC Firmware Update Utility. These are:

- Problem: Displays the message "Display Firmware Management Information" upon booting.
- Implication: An inconvenience; no adverse implications.
- Problem: Command-line switches not implemented.
- Implication: No command-line interface available; menu-driven only.
- Problem: Utility does not reset after an update. Instead it powers down.
- Implication: Automation is difficult and requires power down following an update.
Note: This will not be fixed, due to an architecture limitation.
- Problem: Utility cannot be used after loading himem.sys.
- Implication: Himem.sys allows other programs that use the upper memory, instead of conventional memory, such as network drivers.
- Problem: Utility must run from a floppy disk.
- Implication: Files cannot be loaded on hard disk drives, which is inconvenient.
- Problem: Utility does not display revision or version number.
- Implication: No adverse implications.
- Problem: Utility does not display company name or copyright information.
- Implication: No adverse implications.

- Problem: Utility does not display usage information with the '/?' command-line switch.
- Implication: No adverse implications because the utility does not have command-line options.
- Problem: Key mappings could be altered after usage, but before reboot.
- Implication: An inconvenience. No adverse implications because a power down is required after an update.
- Problem: Utility requires a minimum of 377 KB of conventional memory to run.
- Implication: This could inhibit the use of network drives to load the BMC firmware.

Workaround Use BMCSRUP.EXE Version 2.21.

Status: Fixed. The above problems are fixed in version 2.21, except for the noted power issue, as noted above.

24. TCO packet processing issue reported

Problem	The TCO (Total Cost of Ownership) feature of the Intel® 82559 network interface controller allows support for remote access to server management features over the local area network. The Intel® 82559 automatically filters packets based on the type of network traffic and routes TCO server management packets to the BMC. This feature is available only when enabled on a server board designed to support Direct Platform Control over LAN. There is an issue that impacts the TCO feature as implemented on the SBT2 server board. Under some circumstances, the Intel 82559 NIC, when functioning with the Receive TCO function enabled, can incorrectly identify fragmented IP packets as server management packets, causing these packets to be transferred by the Intel 82559 to the BMC. This transfer to the BMC occurs over the low-speed serial SMBus. Although this packet makes it into the network stack as well as to the BMC, during the time it is being transferred, additional packets that are received by the Intel 82559 cannot be transferred beyond its 3 KB receive FIFO into system memory. If the FIFO fills up, subsequent packets directed to the Intel 82559 will be dropped until the transfer to the BMC is completed and the FIFO is cleared. If the IP fragment origin was an application or protocol, such as network file system (NFS), that tracks end-to-end delivery of data, then that protocol will retry the transmission, resulting in identical IP fragmentation, and the repeated dropping of a packet or packets.
Implication	Eventually, the application or protocol will cause an error condition, such as dropping the network condition. This issue can affect all SBT2 server boards with BMC firmware version 1.17 or 11.17 and previous versions.
Workaround	By disabling the TCO feature, this will not affect other remote management methods, such as remote / emergency management through the COM2 port by modem. Since DPC over LAN is not a supported feature for the SBT2 server

board, disabling the TCO feature of the Intel 82559 NIC has no effect on the functionality of the SBT2 server board software or utilities.

Status Fixed. Load BMC Firmware Version 1.19. This is available at <http://support.intel.com/support/motherboards/server/SBT2/>

25. BIOS containing Microcode Update fix for 99 MHz B stepping Pentium® III Xeon™ processor erratum

Problem When 933 MHz B stepping Pentium® III Xeon™ processors are installed in the SBT2 server board, the products may be affected by the processor erratum: “High Temperature and Low Supply Voltage Operation May Result in Incorrect Processor Operation”. This erratum is documented in the Intel® Pentium® III Xeon™ Processor Specification Update, order number 244460-027, as erratum #G85.

Implication See the above referenced specification update for details.

Workaround No workaround exists for this erratum.

Status Fixed. Use BIOS Release 1.10 (Build 21) or greater.

26. Incorrect hard drive capacity may be displayed during POST

Problem The STL2 onboard AIC7899 with Bios 2.57.S7 may show an incorrect capacity under [Options]-[SCSI Disk Utilities] when a large harddisk is connected (e.g. 4471 MByte using Seagate ST373405LW).

Implication This issue is merely a display aesthetical inconvenience. This issue has not affected the way any OS listed in the Tested Hardware & OS List sees hard disks.

Workaround This issue will not be fixed in a future release of the STL2 BIOS.

Status NoFix

27. Anomaly when upgrading SBT2 BMC Firmware

Problem When upgrading the SBT2 BMC firmware, the user may encounter an occasional error as follows:

```
BaseBoard Management Controller
Self test Result Failed
Error:0BB0: SMBIOS - SRAM Data read error
Error:0B90: BMC Platform Information Area Corrupted
```

This is an anomaly that Intel has discovered when upgrading the BMC firmware.

Implication Although this may appear fatal, the problem can be worked around. Please see workaround for details.

Workaround In order to get around this, the SBT2 server board firmware must use upgraded twice in a row. If this does not work properly, the board must be returned via the standard RMA process.

Status NoFix.

Documentation Changes

1. Quick Start Guide processor changes

Problem Page 25 reads 900 MHz processor when it should read 933 MHz processor. This error is in both the English and Japanese versions of the document.

Status Fix. This error is fixed in future releases of the Quick Start Guide

2. Product Guide processor changes

Problem Pages 10 and 25 read BX80526KB1000256 when it should read BX80526KB001256. It also reads BX80256KB8001M instead of the correct verbage of BX80526KB933256.

Status Fix. This issue is fixed in future releases of the Product Guide.