

## Connectors

The connectors allow the mainboard to connect electronically with other parts of the system. Some connectors have two pins, others have four or five pins. Some malfunction problems encountered with your system may be caused by loose or improper connections. Ensure that all connections are in place and firmly attached.

CONNECTOR	PIN-OUTS	SIGNAL NAME
CN1 PS/2 Keyboard Connector	1 2, 6 3 4 5	Keyboard data NC Ground + 5V Keyboard clock
CN4 Keyboard Connector	1 2 3 4 5	Keyboard clock NC Ground + 5V Keyboard clock
CN5 Power Connector	1 2, 10, 11, 12 3 4 5, 6, 7, 8 9	Power good + 5V + 12V - 12V Ground - 5V
CN6** 3.3V Daughter Board Connector	1, 3, 14, 16 2, 4, 13, 15 5, 12 6, 11 7, 8, 9, 10	+ 3.3V + 5V Voltage switch signal + 12V Ground
J3 Green Status LED Connector	1 2	LED - LED +
J4* Green Power Supply Connector	1 2	Enable/disable power supply outlet Ground
J5 Turbo LED Connector	1 2	LED + LED -

Table 2-5. Connector Pin Definitions (Continued)

CONNECTOR	PIN-OUTS	SIGNAL NAME
J6 Turbo Switch Connector	1 2	Turbo signal Ground
J7 Reset Switch Connector	1 2	Ground Reset signal
J8 Speaker Connector	1 2 3 4	Speaker signal NC Ground + 5V
J9 Keylock and Power LED Connector	1, 2 3, 5 4	Power LED Ground Keyboard lock
J10 Hardware Sleep Connector	1 2	Hardware sleep signal Ground
J11 CPU Fan Connector	1 2 3	Ground + 12V Ground

- \* Insert two-pin connector wires from Green Power Supply into Connector J4.
- \*\* If you decide not to use the regulator board on the mainboard, the caps on pin 1-2, 3-4, 13-14, 15-16 of CN6 should be replaced.

Table 2-5. Connector Pin Definitions

## VESA Bus Connector

The cache system board provides two high-performance VESA bus connectors, SL7 and SL8, for use with VESA peripherals. The VESA bus connector can be utilized for one Local Bus Master and one Local Bus Slave either (SL7) or (SL8). The following tables give the pin assignments for SL7 and SL8. Side A of the connector are pin outs on the board's component side while Side B are pin outs on the board's solder side. Jumpers JV1 and JV2 give more information on settings on the mainboard and the VL-bus controller.

JUMPER	PIN DEFINITION
JV1	High speed write select 1-2 One wait write 2-3 Zero wait write (default)
JV2	CPU speed select 1-2 Greater than 33MHz 2-3 Less than or equal to 33MHz (default)

Table 2-6. VL-Bus Controller Jumper Setting