JUMPER	486SX/ P23S* (PGA)	P24S*/P4S* P24C#/ 486DX/ IntelDX2™ (PGA)	P24CT/P24T (PGA)	Cx486S (M6) (PGA)	Cx486DX (M7) Cx486S+ Cx487S (M6+C6) (PGA)	P24D* (PGA)
JC1, JC2	2-3	1-2	1-2	2-3	1-2	1-2
JC3	open	open	shorted	open	open	open
JC4	1-2	1-2	2-3	1-2	1-2	2-3

* P23S, P24S, P24D, and P4S are the SL-enhanced CPUs while P24T is the Overdrive Processor.

When the onboard 3.3 volt regulator is not present, the 3.3 volt daughter board should be installed. If not , please refer to page 2-12 installation of the 3.3 volt regulator daughterboard.

JUMPER (RP 0Ω 8p4R)	P23S/P4S/ P24S/P24T (PGA)	486SX/DX/ IntelDX2™ (PGA)	Cx486S/DX (PGA)	P23C/P24D/ P24CT (PGA)
JC5, JC6	shorted	open	open	shorted
RN23	empty	empty	inserted	empty
RN29	empty	empty	empty	inserted

JUMPER	PIN DEFINITION
J20	P24C Clock Mode Select Open 3 X mode 1-2 2 X mode 2-3 2.5 X mode
JCX1	1-2 Intel SL-enhanced CPU, others (default) 2-3 Cyrix Cx486S/DX
JP2, JX1	1-2 * IRQ15 (Regular CPUs – default) 2-3 – SMI (Cyrix or Intel SL-enhanced CPUs)
JP1, JP3, JP5	1-2 (factory default)

* IRQ15 is no longer available for the other devices when SMI is selected.

Table 2-1. Jumper Settings for CPU Selector