→ **NOTE : Users are not encouraged to change the jumper settings not listed in this manual. Changing the jumper settings improperly may adversely affect system perform-ance.**

ISA/VESA Bus Connector

The mainboard utilizes two VESA connectors to provide high-performance ISA/VESA bus path, SL1 and SL2, for use with ISA/VESA peripherals. The latter VESA bus connector, SL 2, can be utilized for one Local Bus Master or one Local Bus Slave. The two connectors supports one optional riser card with 2 PCI, 1 VESA and 1 ISA bus slots on it.

The following tables give the pin assignments for SL1 and SL2. Side A of the connector are pin-outs on the board's component side while Side B are pin-outs on the board's solder side. Jumpers JL1 and JL2 give more information on settings on the mainboard and the VL-bus controller.

JUMPER	PIN DEFINITION
JV2	High Speed Write Select 1-2 Zero wait write (default) 2-3 One wait write
JV3	CPU Speed Select 1-2 Less than or equal to 33MHz (default) 2-3 Greater than 33MHz

Table 2-5. Jumper Settings of SL1, SL2