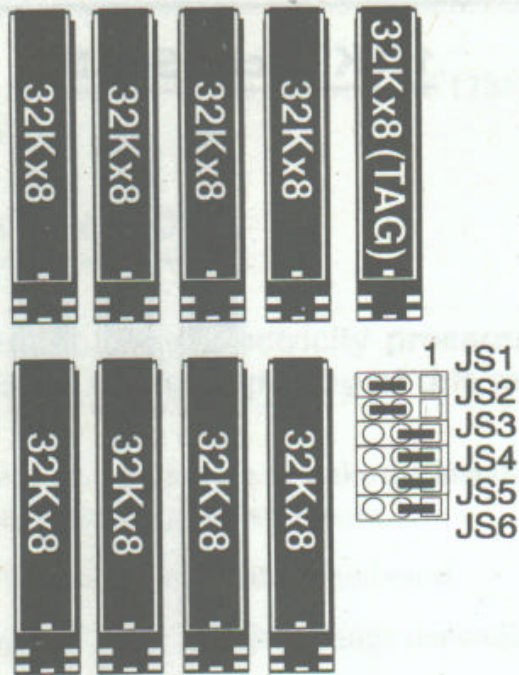


**256K Cache SRAM****OR**