

3. Carefully push the SIMM down and back into the socket until the retaining clips of the socket snap, holding the SIMM in place. The holes in the SIMM should match the pins on the socket's retaining clips.

To remove the SIMM(s), pull the retaining latch on both ends of the socket and reverse the procedure above.

Cache Memory

The PA-1000 supports direct-mapped cache systems with data size ranging from 256KB, 512KB, to 1MB. Both write-back and write-through schemes are supported with maximum flexibility in selecting the number of tag and alter bits. The minimum number of tag plus alter bits is eight to fit the standard x8 SRAMs. To allow a larger cacheable region, more than one x8 SRAMs are required for wider combined tag plus alter bits. The PA-1000 supports tag bits up to ten in addition to the alter bit for the write-back scheme.

→ **NOTE : Be sure to use the correct chips for the amount of cache memory you want to add. You must install both the correct Cache and Tag SRAM. Alter RAM type is the same as Tag RAM.**

Installing Cache Memory

→ **NOTE : Always observe static electricity precautions. See "Handling Precautions" at the beginning of this manual.**

If you do not have the confidence to make the installation, better consult a service technician for assistance.

1. Locate the cache memory on the mainboard.
2. Be guided by the Cache SRAM settings depending on your desired SRAM configuration.

Correct orientation of the chip is necessary for the cache to operate properly. Normally, the chips have either a curved notch or a dot. This marker on the chip must be matched to the marker on the socket for correct alignment.

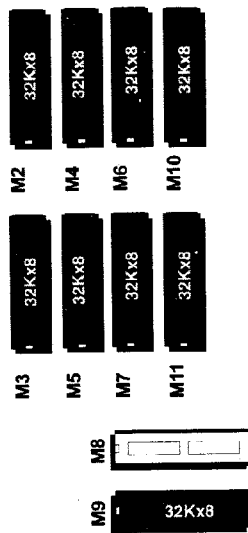
PA-1000

Install the chips individually as follows:

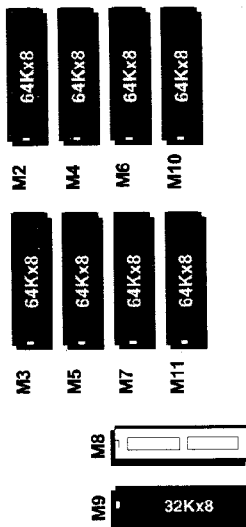
3. Align the chip with the marker on the socket. Press the chip onto the socket, ensuring that the pins on the chip are aligned with the corresponding connections on the socket.
4. Press the chip completely into the socket so that the pins are properly seated.

Cache SRAM Specifications and Settings

256K Cache SRAM



512K Cache SRAM



PA-1000