

2). Install DRAM Modules

DRAM Memory

The working space of the computer is the Random Access Memory. The system cannot act upon data unless it is loaded into RAM. A system RAM is comprised of industry-standard 72-pin Single In-line Memory Modules (SIMMs). The DRAMs are installed onto the SIMM expansion slots.

Extended Data Out (EDO) memory are the latest DRAM chip designs that perform a lot better than the fast page mode DRAM type. With EDO memory, CPU access to memory is 10 to 15% faster.

When more RAM is added, the working area of the computer is larger, thereby increasing total performance. You should verify the type and speed of the RAM currently installed from your dealer. Mixtures of RAM types, other than those described in this manual, will have unpredictable results.

The PAC-2003 is able to support standard FPM (Fast Page Mode), or EDO (Extended Data Out) DRAM; and can accommodate onboard memory from 8 to 512MB using SIMMs (Single-In-Line Memory Modules). The mainboard has two memory banks — Bank 0 and Bank 1. Each bank has two SIMM sockets which can accept either a pair of 4MB, 8MB, 16MB or 32MB SIMM in each socket.

Banks 0 and 1 can use different types of SIMMs (e.g., 4 or 16MB). However, you must populate each memory bandwidth with the same type of SIMM. For example, Bank0 may contain two 8MB SIMMs and Bank1 may contain two 4MB SIMMs.

DRAM Configuration

DRAM modules can be installed in a variety of configurations, as shown below:

TOTAL MEMORY	BANK0 (72-PIN x 2)	BANK1 (72-PIN x 2)
8MB	4MB & 4MB	
12MB	4MB & 4MB	2MB & 2MB
16MB	8MB & 8MB	
	4MB & 4MB	4MB & 4MB
24MB	8MB & 8MB	4MB & 4MB
32MB	8MB & 8MB	8MB & 8MB
	16MB & 16MB	
40MB	16MB & 16MB	4MB & 4MB
48MB	16MB & 16MB	8MB & 8MB
64MB	16MB & 16MB	16MB & 16MB
	32MB & 32MB	
72MB	32MB & 32MB	4MB & 4MB
80MB	32MB & 32MB	8MB & 8MB
96MB	32MB & 32MB	16MB & 16MB
128MB	32MB & 32MB	32MB & 32MB
	64MB & 64MB	
256MB	64MB & 64MB	64MB & 64MB
512MB	128MB & 128MB*	128MB & 128MB*



NOTE :

1. All memory banks use 72-pin memory modules.
2. * A SIMM of this size was not available yet for testing when this book was printed.