

JUMPER	PIN DEFINITION
J1	RTC Reset (DS12887A only) Short Reset Open Do not reset (default)
J7	Password Clear Short Enabled Open Disabled
J11	Internal Write Back/Through Cache Short Write through Open Write back (default)
J12	CPU Internal Parity Check Short Supports Open Does not support (default)
J15	Hardware Reset Short Reset Open No reset (default)

Table 2-1. Jumper Definitions

JUMPER	PIN DEFINITION
	1-2 * 2-3 **
J11	IRQ5
J12	IRQ3 (default)
J13	IRQ10
J14	IRQ11
J15	IRQ12
J16	IRQ14
J17	IRQ15

* When using the PCI1 slot of your motherboard, jumper choice should only be limited to pin 1-2 of jumpers J11 to J17.

** When using PCI2 or PCI3 slots of your motherboard, jumper choice should only be limited to pin 2-3 of jumpers J11 to J17.

Table 2-2. PCI IRQ Jumper Definitions (Continued)

→ NOTE : Except for PCI IDE add-on cards which conforms to the "Falling" or "Rising Edge Sensitive" specification (see next page), every "INTR#" signal of PCI boards is set at "INTR_A".

JUMPER	PIN DEFINITION
J18	PCI IDE IRQ14 Select Open Level trigger (default) 1-2 Falling edge (TEKRAM DC-690B, ATRONICS IDE-2015P) 2-3 Rising edge (CMD CSA6400C)

Table 2-2. PCI IRQ Jumper Definitions



NOTE : Accordingly, the PCI INTR# signal should conform to the "Level Sensitive" specification. Due to the fact that some PCI IDE are "Falling" or "Rising Edge Sensitive", Jumper J18 was added for the "Edge Trigger" function (IRQ14 only).

PCI IDE Card Installation Instructions

To install the PCI IDE add-on card in your motherboard:

1. Set your PCI IDE add-on card's "INTR#" signal to "INTR_D" only if it conforms to the "Falling" or "Rising Edge Sensitive" specification. Otherwise, it should be set at "INTR_A".
2. Insert the PCI IDE add-on card into the PCI1 slot of the PM-900 motherboard.
3. If the PCI IDE add-on card's "INTR#" signal conforms either to the "Rising or Falling Edge Sensitive" specification, remove the jumper from J11 - J17 pin 1-2 and place it at jumper J18. Jumper need not be removed from J11 - J17 if your PCI IDE add-on card conforms to the "Level Sensitive" specification.